



MSME-Tool Room, Guwahati

Tool Room & Training Centre, Guwahati



Course Time Table

SUBJECT: DIGITAL SYSTEM FROM GATES TO PROCESSOR
PER DAY: 3 hrs

DURATION: 2 weeks

OBJECTIVE:

At the end of Session Participants Should Know:

- * Develop a Digital Logic and apply it to Solve Real-life problems.
- * Simulate and Implement Combinational and Sequential Circuits Using VHDL Systems.
- * Analyze Digital System Design Using Processors.

Day	Course Content
Day 1	✓ Number System ✓ Introduction to Logic Gates
Day 2	✓ Combinational Logic Circuits ✓ Boolean Algebra
Day 3	✓ K-Map ✓ SOP & POS
Day 4	✓ Multivibrator Circuits ✓ Flip-Flops
Day 5	✓ Arithmetic Circuits ✓ Half and Full Adder ✓ Half and Full Subtractor
Day 6	✓ Parallel addition and Subtraction using Integrated Circuits ✓ Counters
Day 7	✓ Coders ✓ Multiplexers
Day 8	✓ Register ✓ RTL (Register Transfer Level)
Day 9	✓ Universal Shift Register ✓ Verilog Data Types
Day 10	✓ Programmable logic Array ✓ RAM vs ROM
Day 11	✓ Operational Amplifier ✓ Data Communication
Day 12	✓ Analog to Digital conversion ✓ Digital to Analog Conversion
Day 13	✓ Introduction to Microprocessors ✓ 8085 up
Day 14	✓ Instruction set ✓ Processor interfacing

N.B:

*Classes & Demonstration will be conducted every day for a time period of one hours & doubt clear Session of one hour with all day online/on-call support.

*Hard copy of Certificate will be send to Participant's address by post or it may also be collected from our Centre when situation get normalized.

*Each trainee can avail a free one-week Internship (Practical/Project based) training at our Centre after this pandemic situation get normalized.

You may contact with coordinator for course related further information.

Course Coordinator: SudeepBasak

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